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PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of

Docket No: Q88131

Hitoshi YANO, et al.

Appln. No.: 10/537,470

Group Art Unit: 2817

Confirmation No.: 3255

Examiner: Not Yet Assigned

Filed: June 3, 2005

For: AMPLIFYING CIRCUIT

INFORMATION DISCLOSURE STATEMENT
UNDER 37 C.F.R. §§ 1.97 and 1.98

MAIL STOP AMENDMENT

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

Sir:

In accordance with the duty of disclosure under 37 C.F.R. § 1.56, Applicant hereby notifies the U.S. Patent and Trademark Office of the documents which are listed on the attached PTO/SB/08 A & B (modified) form and listed herein and which the Examiner may deem material to patentability of the claims of the above-identified application.

1. Japanese Patent Application Publication No. 2000-349574, published December 15, 2000 (previously submitted in the Information Disclosure Statement filed June 3, 2005).
2. Japanese Patent Application Publication No. 2001-267865, published September 28, 2001.
3. Japanese Patent Application Publication No. 6-283938, published October 7, 1994.
4. Japanese Patent Application Publication No. 2002-217657, published August 2, 2002.
5. Japanese Patent Application Publication No. 2002-141752, published May 17, 2002.

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6. Japanese Patent Application Publication No. 3-195108, published August 26, 1991 (previously submitted in the Information Disclosure Statement filed June 3, 2005).
7. Japanese Patent Application Publication No. 2002-271152, published September 20, 2002 (previously submitted in the Information Disclosure Statement filed June 3, 2005).
8. Japanese Patent Application Publication No. 2002-185270, published June 28, 2002 (previously submitted in the Information Disclosure Statement filed June 3, 2005).
9. Japanese Patent Application Publication No. 2002-135060, published May 10, 2002 (previously submitted in the Information Disclosure Statement filed June 3, 2005).
10. Japanese Patent Application Publication No. 10-126164, published May 15, 1998.

One copy of each of the listed documents is submitted herewith, except for those indicated as being previously submitted.

The present Information Disclosure Statement is being filed: (1) No later than three months from the application's filing date; (2) Before the mailing date of the first Office Action on the merits (whichever is later); or (3) Before the mailing date of the first Office Action after filing a request for continued examination (RCE) under § 1.114, and therefore, no Statement under 37 C.F.R. § 1.97(e) or fee under 37 C.F.R. § 1.17(p) is required.

In compliance with the concise explanation requirement under 37 C.F.R. § 1.98(a)(3) for foreign language documents, Applicant encloses herewith a copy of a corresponding Japanese Office Action, dated May 10, 2006, and an English translation of the pertinent portions thereof which cites such documents and indicates the degree for relevance found by the foreign office.

The submission of the listed documents is not intended as an admission that any such document constitutes prior art against the claims of the present application. Applicant does not waive any right to take any action that would be appropriate to antedate or otherwise remove any listed document as a competent reference against the claims of the present application.


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The USPTO is directed and authorized to charge all required fees, except for the Issue Fee and the Publication Fee, to Deposit Account No. 19-4880. Please also credit any overpayments to said Deposit Account.

Respectfully submitted,


Howard L. Bernstein
Registration No. 25,665

SUGHRUE MION, PLLC
Telephone: (202) 293-7060
Facsimile: (202) 293-7860

WASHINGTON OFFICE

23373

CUSTOMER NUMBER

Date: August 10, 2006

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MODIFIED PTO/SB/08 A & B (08-03)

Substitute for Form 1449 A & B/PTO

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(use as many sheets as necessary)

Complete if Known

Application Number	10/537,470
Confirmation Number	3255
Filing Date	June 3, 2005
First Named Inventor	Hitoshi YANO
Art Unit	2817
Examiner Name	Not Yet Assigned
Attorney Docket Number	Q88131

Sheet 1 of 1

U.S. PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Document Number		Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document
		Number	Kind Code ² (if known)		
		US			
		US			
		US			
		US			
		US			
		US			
		US			
		US			
		US			

FOREIGN PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Foreign Patent Document			Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Translation ⁶
		Country Code ³	Number ⁴	Kind Code ⁵ (if known)			
		JP	2000-349574	A	12-15-2000		
		JP	2001-267865	A	09-28-2001		
		JP	6-283938	A	10-07-1994		
		JP	2002-217657	A	08-02-2002		
		JP	2002-141752	A	05-17-2002		
		JP	3-195108	A	08-26-1991		
		JP	2002-271152	A	09-20-2002		
		JP	2002-185270	A	06-28-2002		
		JP	2002-135060	A	05-10-2002		
		JP	10-126164	A	05-15-1998		

NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city, and/or country where published.	Translation ⁶

Examiner Signature

Date Considered

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹Applicant's unique citation designation number (optional). ²See Kind Codes of USPTO Patent Documents at www.uspto.gov, MPEP 901.04 or follow the hyperlink from the title of the document to the intranet. ³Enter Office that issued the document, by the two-letter code (WIPO Standard ST. 3). ⁴For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. ⁶Applicant is to indicate here if English language Translation is attached.

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(CLAIMS 1–3 AND 9)

Cited literature: 1–5

In a variable gain amplification circuit configured by connecting a plurality of amplification circuits in parallel, controlling the input impedance or output impedance of amplification circuits which are not operated so as to keep it at a high impedance is well known art, as described for instance in the belowmentioned Cited Literature 1–5.

(CLAIMS 4–7)

Cited literature: 1–5

Literature 3–5 disclose forming an arrangement for high impedance control using switch elements. In particular, Literature 4 describes inductance elements and switch elements being connected in series, and Literature 3 describes a configuration based on field effect transistors.

The specific connection arrangement, etc. are matters of design for a person skilled in the art, and nothing remarkable is found in the matters specified in these claims.

(CLAIM 8)

Cited literature: 1–6

An arrangement whereby two quarter-wave transmission lines are switched with a switch element is also publicly known as an impedance regulation circuit, and nothing remarkable is found in the matters specified in these claims.

If any reasons for rejection are newly discovered, a notification of reasons for rejection will be issued.

List of Cited Literature

1. Japanese Unexamined Patent Application Publication 2000-349574
2. Japanese Unexamined Patent Application Publication 2001-267865
3. Japanese Unexamined Patent Application Publication H6-283938
4. Japanese Unexamined Patent Application Publication 2002-217657
5. Japanese Unexamined Patent Application Publication 2002-141752
6. Japanese Unexamined Patent Application Publication H3-195108

Record of Prior Art Literature Search Results

- Fields searched IPC 7th Edition H03G 1/00–3/34
H03F 3/68
- Prior art literature Japanese Unexamined Patent Application Publication 2002-271152
Japanese Unexamined Patent Application Publication 2002-185270
Japanese Unexamined Patent Application Publication 2002-135060
Japanese Unexamined Patent Application Publication H10-126164